In re application of: Stepan Sokolov	Attorney Docket No.: SUN1P840/P6721
Application No.: 09/938,915	Examiner: KENDALL, CHUCK O.
Filed: August 24, 2001	Group: 2122
Title: FRAMEWORKS FOR GENERATION OF JAVA MACRO INSTRUCTIONS FOR PERFORMING PROGRAMMING LOOPS	Confirmation No. 3300

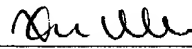09/06/2005 FMETEKI1 00000024 09938915

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INFORMATION DISCLOSURE STATEMENT
AFTER FINAL ACTION OR NOTICE OF ALLOWANCE
(37 CFR §§ 1.56 AND 1.97)

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of Non-US references are attached, may be material to examination of the above-identified patent application. Applicants submit identify these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is being filed after the mailing date of a notice of allowance under §1.311, but before payment of the issue fee.

Accompanying this Information Disclosure Statement is the fee set forth in 37 CFR 1.17(p).

Enclosed is our Check No. 27669 for $180.00 in payment of the Information Disclosure Statement Fee. If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SUN1P840).

Respectfully submitted,
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Form 1449 (Modified)	Atty. Docket No. SUN1P840/P6721	Application No.: 09/938,915
Information Disclosure Statement By Applicant	Applicant: Stepan Sokolov	
(Use Several Sheets if Necessary)	Filing Date August 24, 2001	Group 2192

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1	2002/0188726A1	12/12/02	Schick et al.			06/08/01
	A2	2002/0170041A1	11/14/02	Shann			12/20/01
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	A5	5,815,718	09/29/98	Tock			05/30/96
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Examiner				Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

| Form 1449 (Modified)

Information Disclosure
Statement By Applicant

(Use Several Sheets if Necessary) | Atty. Docket No.
SUN1P840/P6721
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09/938,915

Group
2192 |

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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	No
	B1	EP0810522 A2	12/03/97	EPO				
	B2	EP0943989 A2	09/22/99	EPO				
	B3	WO99/31576	06/24/99	PCT				

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	Patrice Pominville, "Annotating Java Bytecode," Project Report, McGill University, Online, April 2000, pages 1-7. XP-002254149.
	C2	Yourst, "Inside Java Class Files," Dr. Dobb's Journal, Jan. 1998, XP-002254150.
	C3	Hummel et al., "Annotating the Java bytecodes in support of optimization," Concurrency: Practice and Experience, John Wiley and Sons, Vol. 9(11), November 1997, pages 1003-1016. XP-001131083.
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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C4	Lindholm et al., "The Java Virtual Machine Specification," Second Edition, April 1999, pages 117-132. XP-002254151.
	C5	Dahm, Markus, "Byte Code Engineering", 1999, Java Information Tag, Proceedings of JIT' 99: Java-Informations'Tage, Dusseldorf, Germany, Vol. 20-21, pp. 267-277, XP-002262007.
	C6	Bill Venners, "How the Java virtual machine handles method invocation and return" Java World, June 1997, pages 1-10.
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	C12	Nicol et al., "Empirical Study of parallel Trace-Driven LRU Cache Simulators", p. 166-169, 1995 IEEE.
	C13	Clausen et al., " Java Bytecode Compression for Low-End Embedded Systems," 5/2000, ACM.

Examiner	Date Considered

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